

# ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

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## Title of Invention

INTEGRATED CIRCUIT YIELD ENHANCEMENT USING  
VORONOI DIAGRAMS

Application Number :

Confirmation Number:

First Named Applicant: Robert Allen

Attorney Docket Number: BUR920030135US1

Art Unit:

Examiner:

Search string: ( 6178539 or 6247853 or 6317859 ).pn

## US Patent Documents

**Note: Applicant is not required to submit a paper copy of cited US Patent Documents**

| init | Cite.No. | Patent No. | Date       | Patentee            | Kind | Class | Subclass |
|------|----------|------------|------------|---------------------|------|-------|----------|
|      | 1        | 6178539    | 2001-01-23 | Papadopoulou et al. |      |       |          |
|      | 2        | 6247853    | 2001-06-19 | Papadopoulou et al. |      |       |          |
|      | 3        | 6317859    | 2001-11-13 | Papadopoulou et al. |      |       |          |

## Signature

|               |      |
|---------------|------|
| Examiner Name | Date |
|               |      |